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## APPLICANTS

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\*\* CONTINUING DATA <sup>N</sup> <sup>VS</sup> \*\*\*\*\*

\*\* FOREIGN APPLICATIONS <sup>N</sup> <sup>VS</sup> \*\*\*\*\*

IF REQUIRED, FOREIGN FILING LICENSE  
 GRANTED

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|   |          |         |        |             |
|---|----------|---------|--------|-------------|
| Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no                                | STATE OR | SHEETS  | TOTAL  | INDEPENDENT |
| 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met | COUNTRY  | DRAWING | CLAIMS | CLAIMS      |
| Verified and Acknowledged<br><i>W. J. Peters</i><br>Examiner's Signature<br><i>VS</i><br>Initials                           | WA       | 4       | 20     | 2           |

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## TITLE

Method of mapping logic failures in an integrated circuit die

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